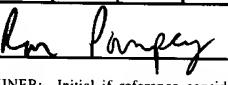


Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE LIST OF ART CATEGORIES APPLICANT (Use several sheets if necessary)					ATTY. DOCKET NO. MI22-532		SERIAL NO. 09/332,271	
					APPLICANT Klaus Florian Schuegraf et al.			
					FILING DATE June 11, 1999		GROUP 2812	
U.S. PATENT DOCUMENTS								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
RP	AA	6,310,300 B1	10/30/01	Cooney, III et al.				
RP	AB	6,214,730 B1	04/10/01	Cooney, III et al.				
RP	AC	6,066,577	05/23/00	Cooney, III et al.				
RP	AD	5,930,655	07/27/99	Cooney, III et al.				
RP	AE	5,877,074	03/02/99	Jeng et al.				
RP	AF	5,861,340	01/19/99	Bai et al.				
RP	AG	5,767,558	06/16/98	Lo et al.				
RP	AH	5,652,156	07/29/97	Liao et al.				
RP	AI	5,364,803	11/15/94	Lur et al.				
RP	AJ	5,208,182	05/04/93	Narayan et al.				
RP	AK	5,093,700	03/03/92	Sakata				
RP	AL	5,047,831	09/10/91	Katayama				
RP	AM	4,700,215	10/13/87	McPherson				
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
RP	AN	Wolf et al., "Silicon Processing for the VLSI Era - Volume 1: Process Technology," ©1986 Lattice Press, pages 384-385 (4 pages total)						
	AO							
	AP							
					12-30-08			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								

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